

FIG. 1

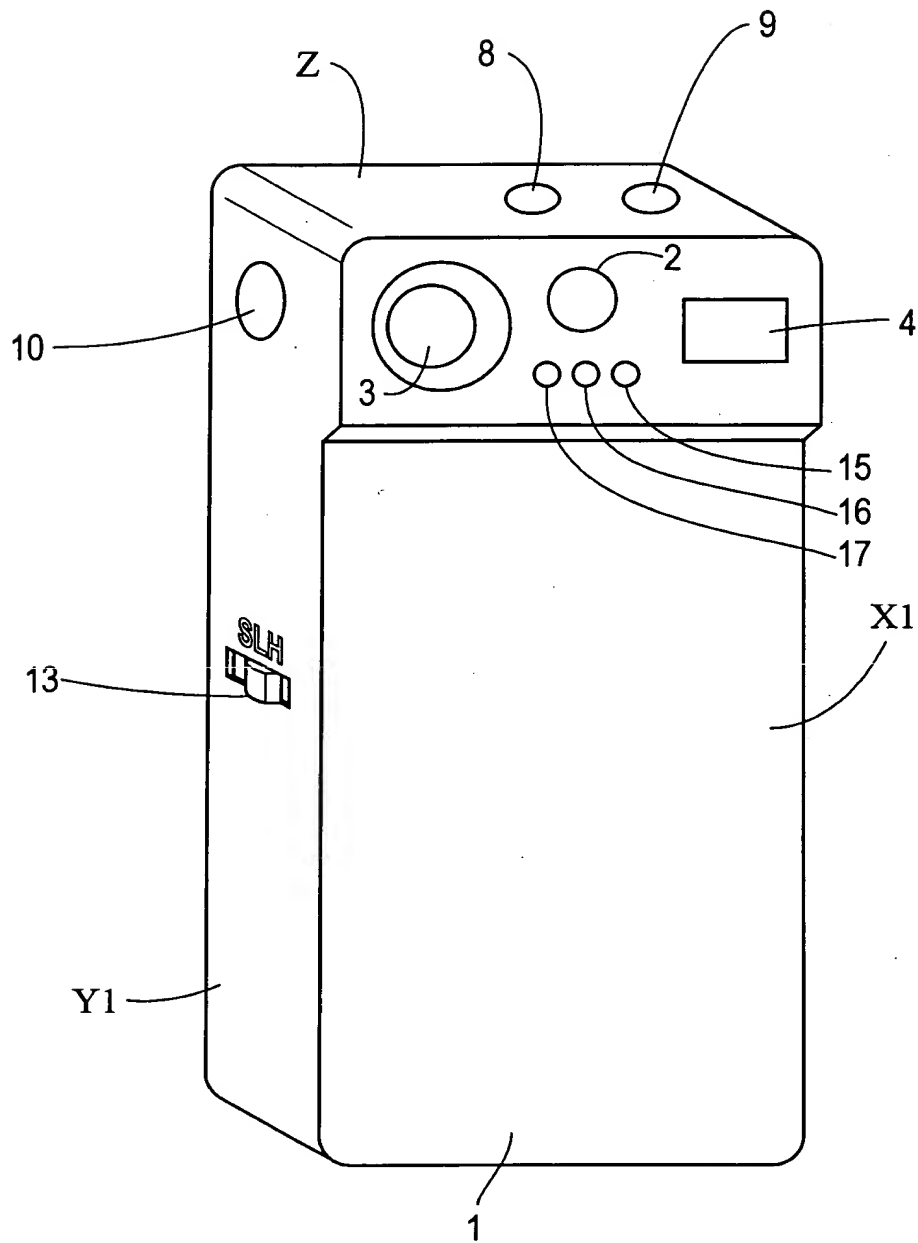


FIG. 1

FIG. 2

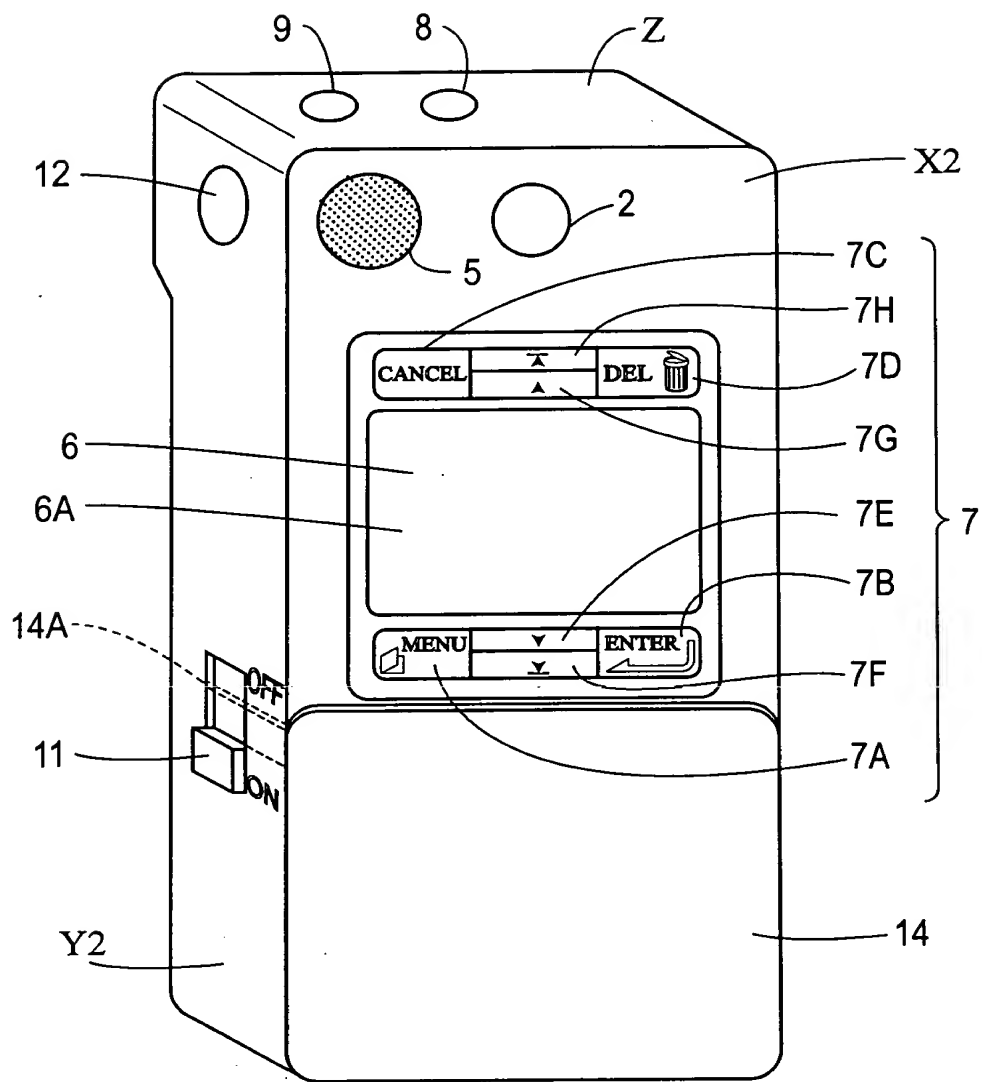


FIG. 2

FIG. 3

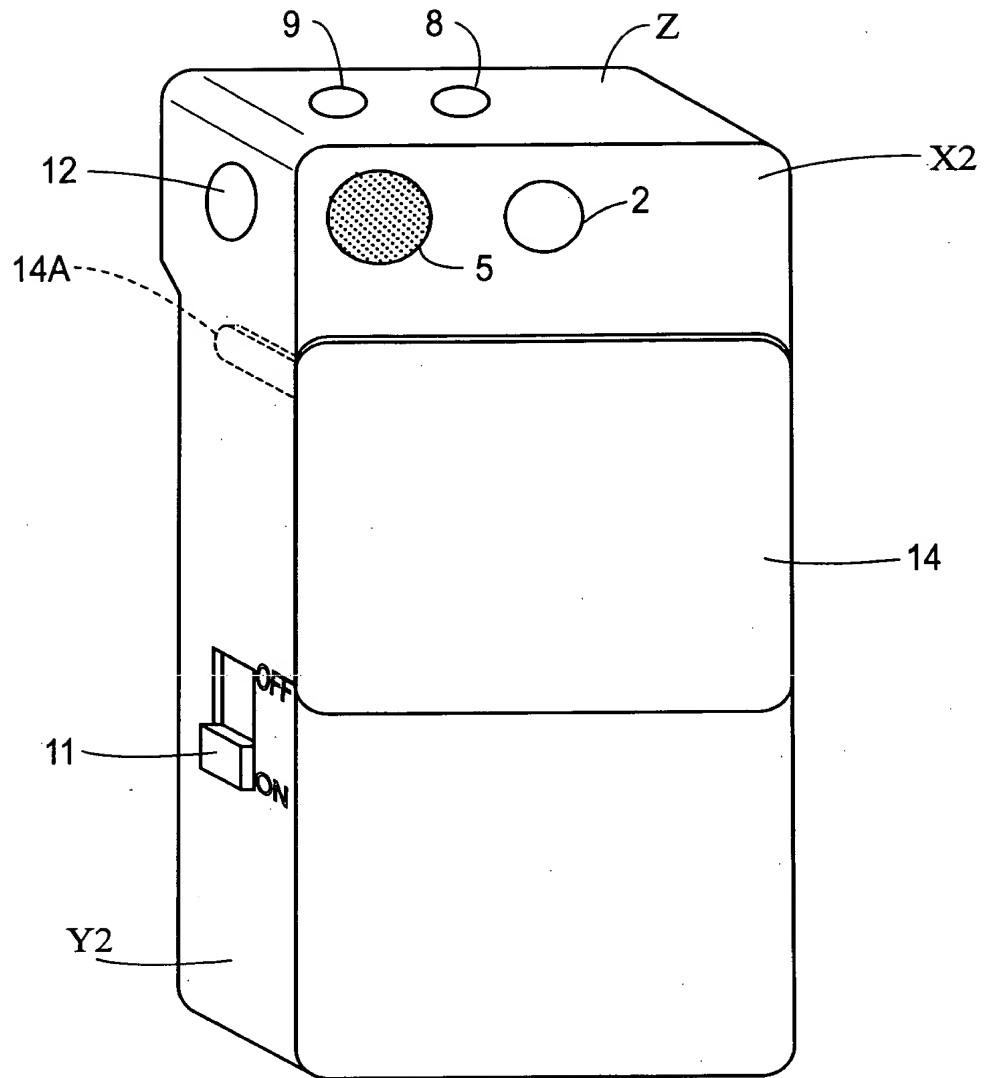


FIG. 3

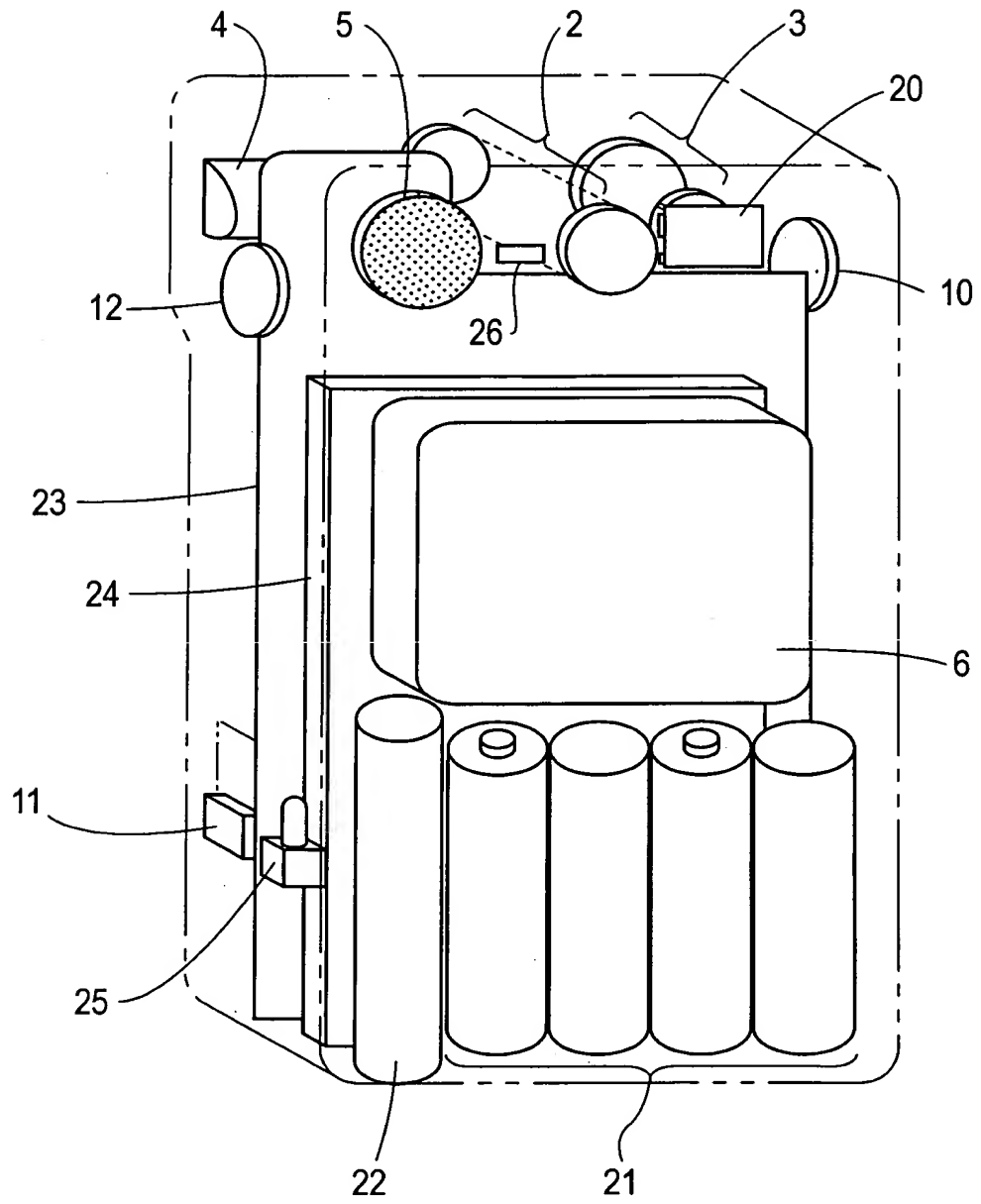


FIG. 4

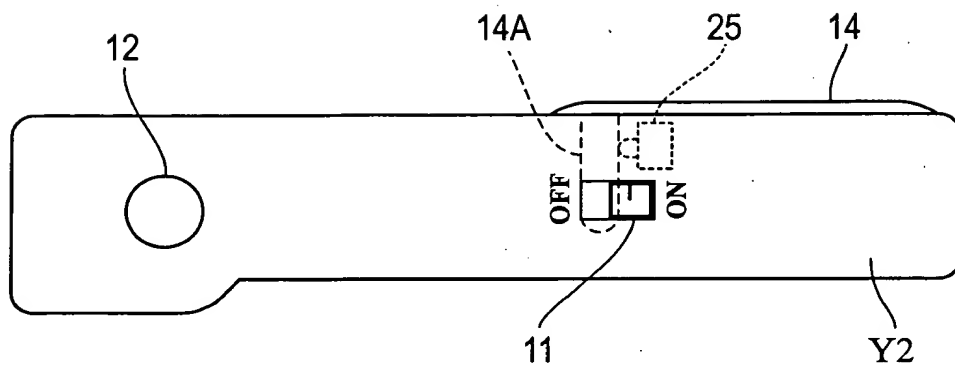


FIG. 5 (A)

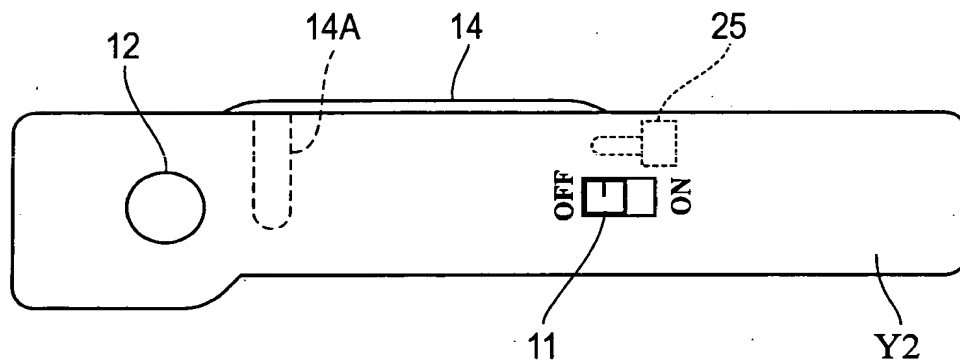


FIG. 5 (B)

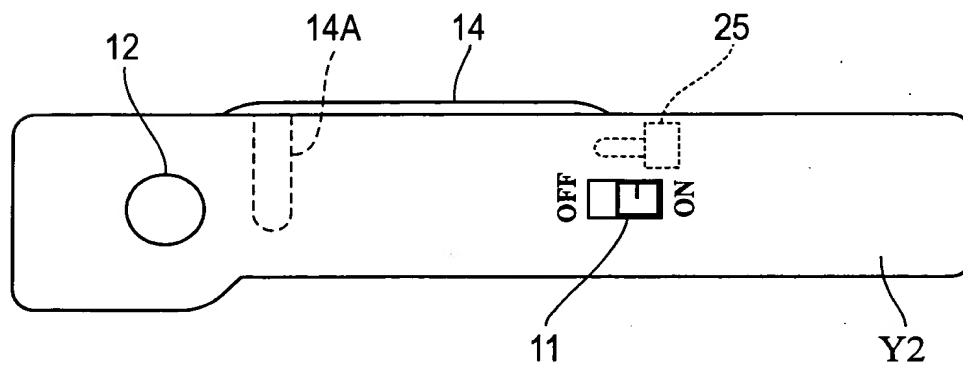


FIG. 5 (C)

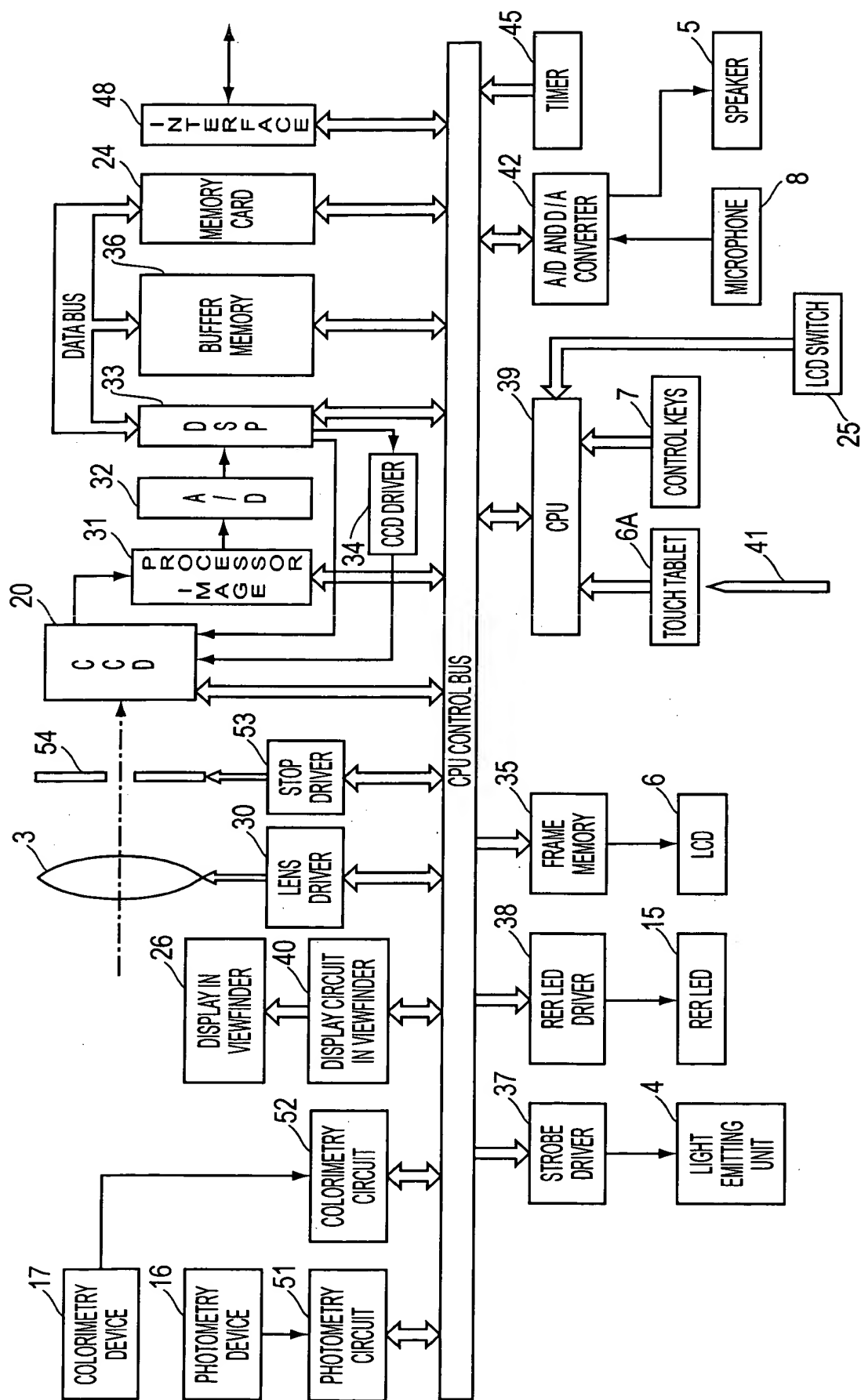


FIG. 6

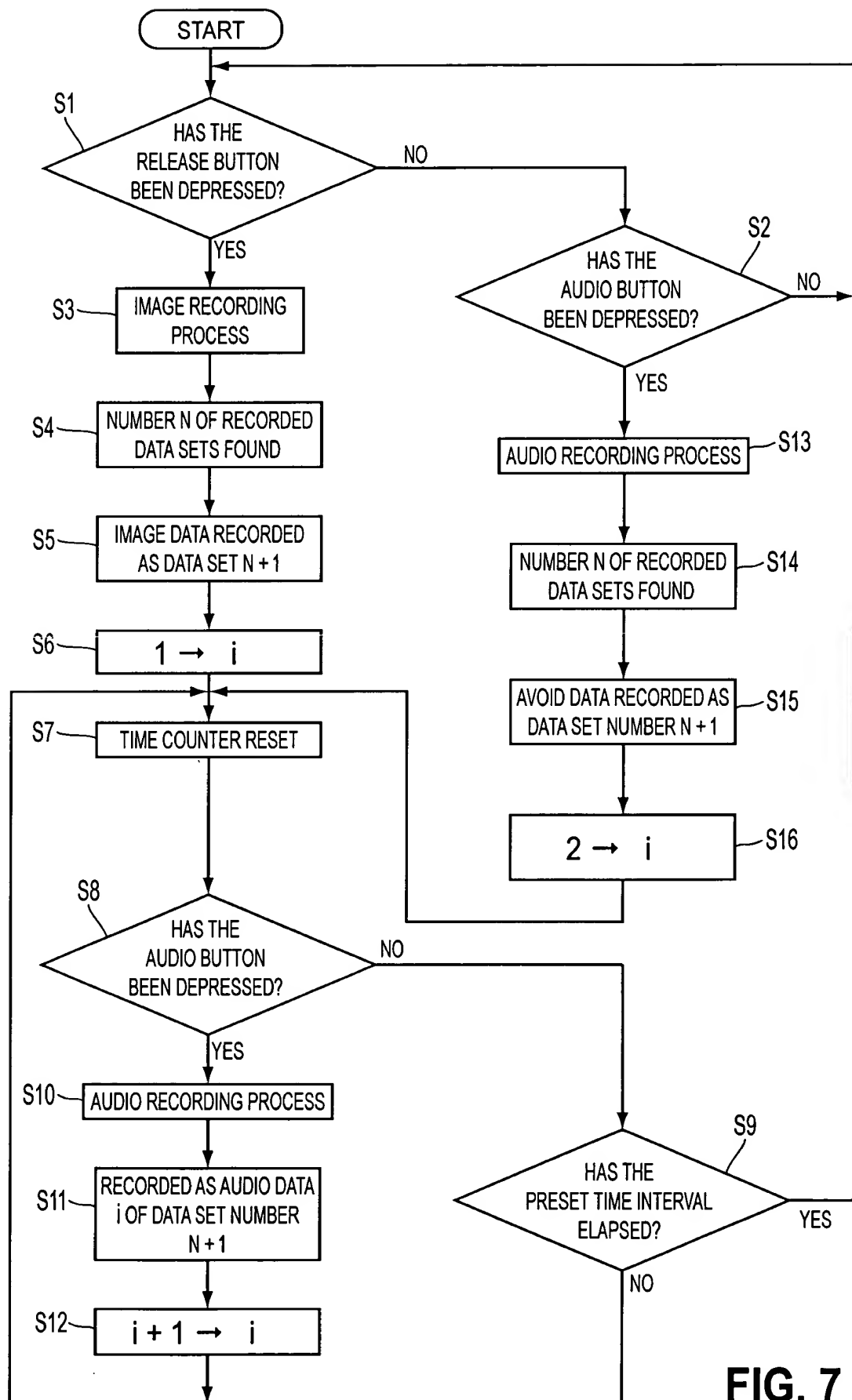


FIG. 7

ADDRESS
#0000

MEMORY
SPACE

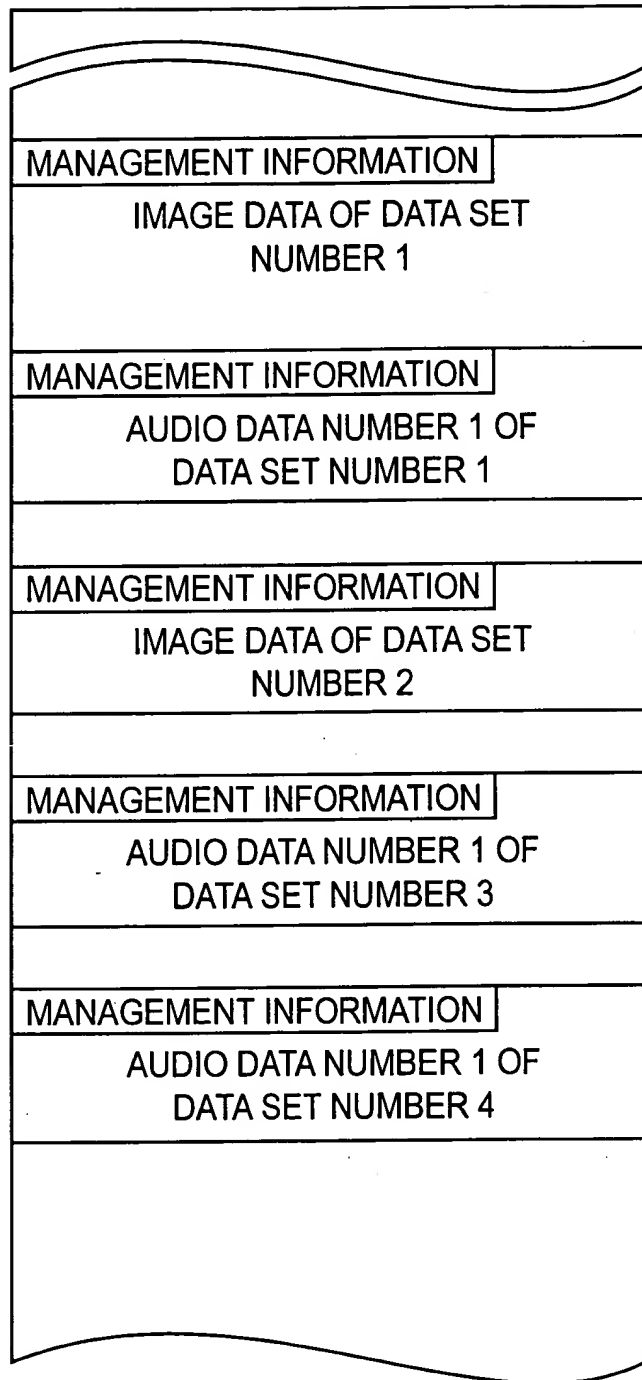


FIG. 8

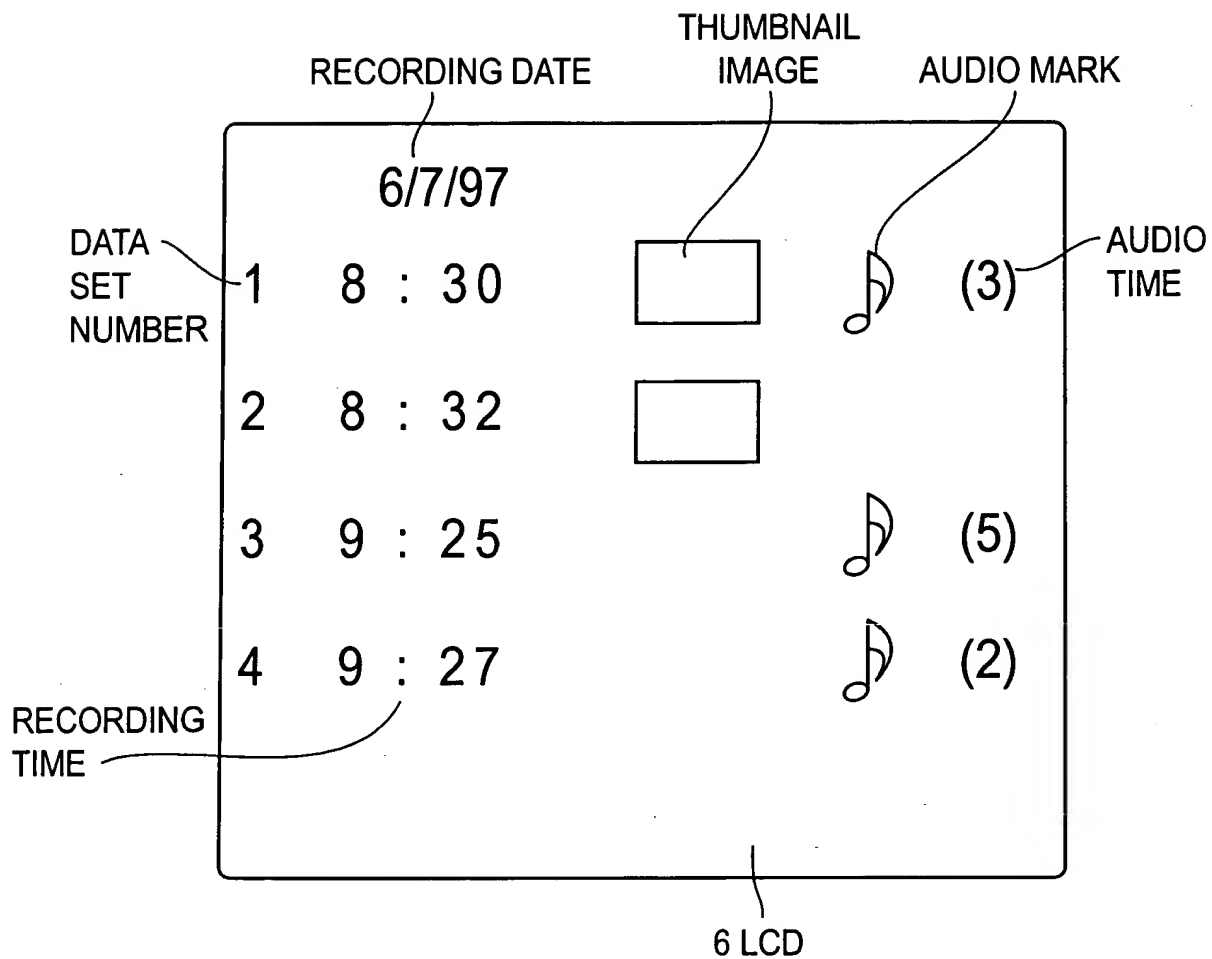


FIG. 9

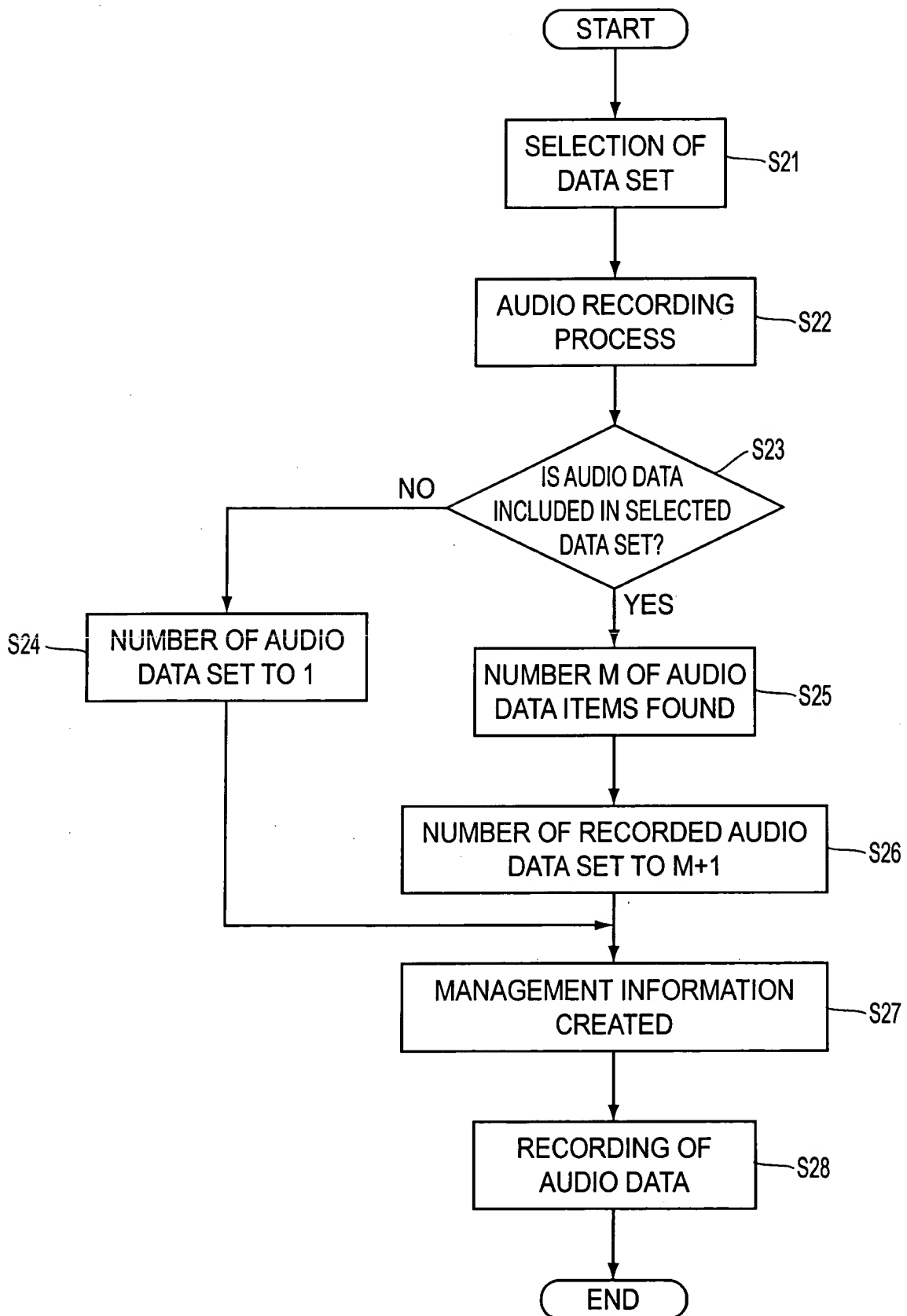


FIG. 10

ADDRESS
#0000

MEMORY
SPACE

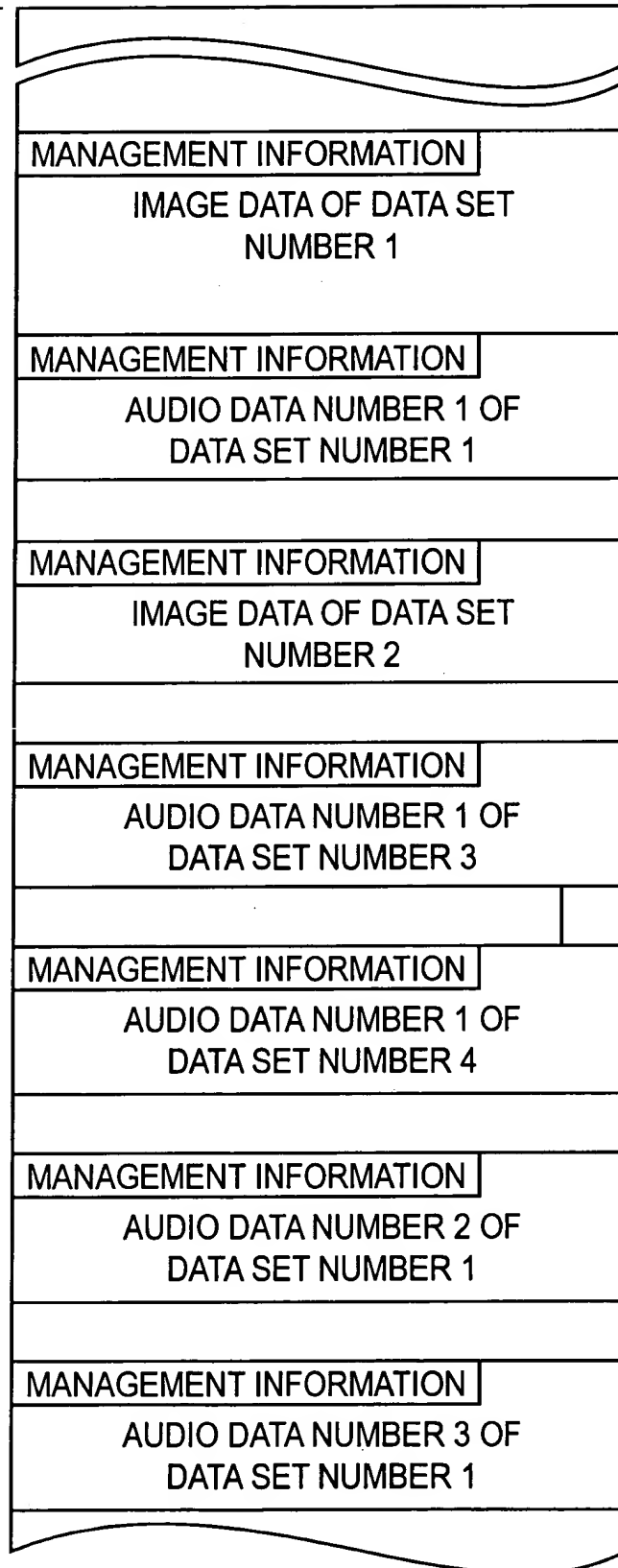


FIG. 11

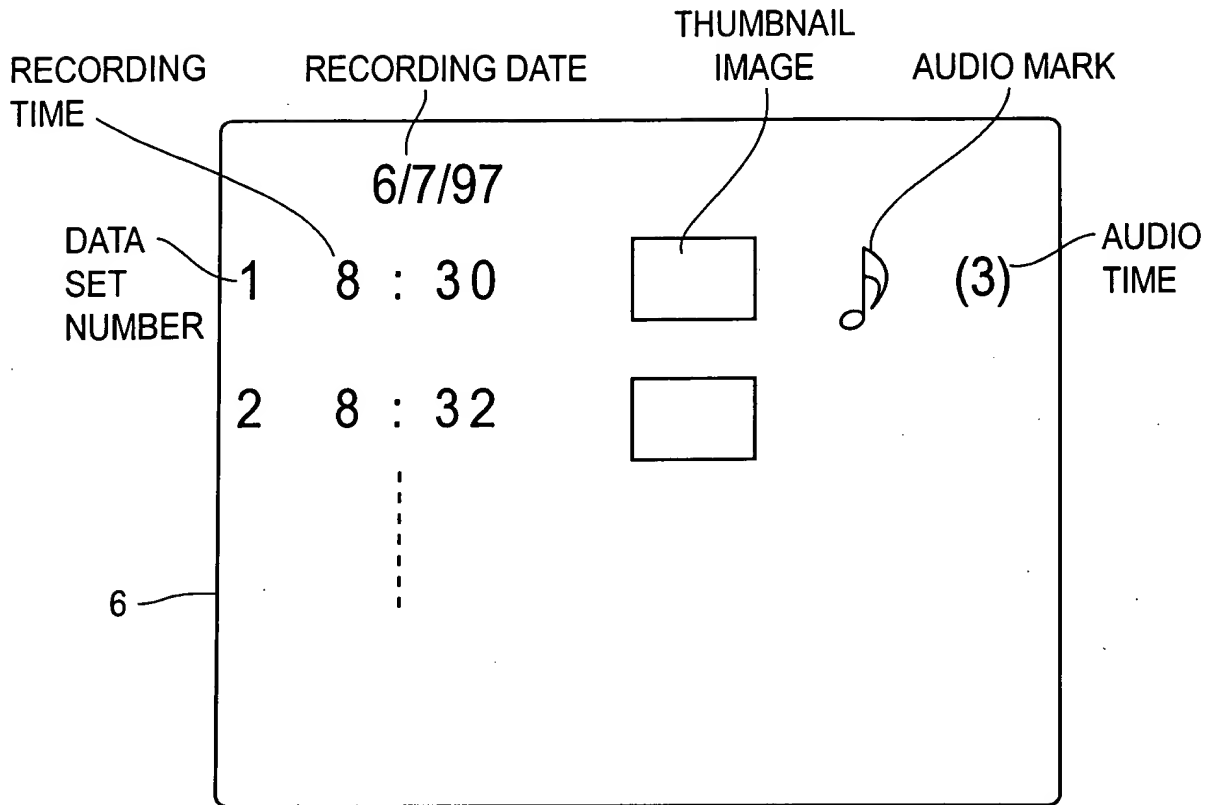


FIG. 12(A)

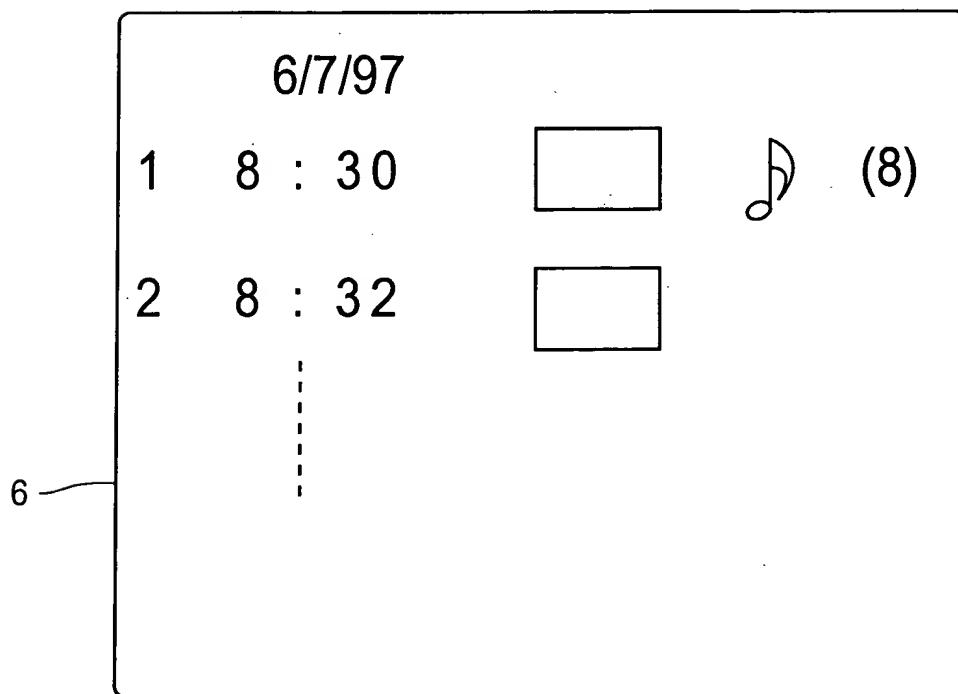





FIG. 12(B)

6/7/97				
1	8 : 30	<input type="text"/>		(10)
2	8 : 32	<input type="text"/>		
	⋮			

6/7/97				
1	8 : 30	<input type="text"/>		(10)
2	8 : 32	<input type="text"/>		
	⋮			

6/7/97				
1	8 : 30	<input type="text"/>		(10)
2	8 : 32	<input type="text"/>		
	⋮			

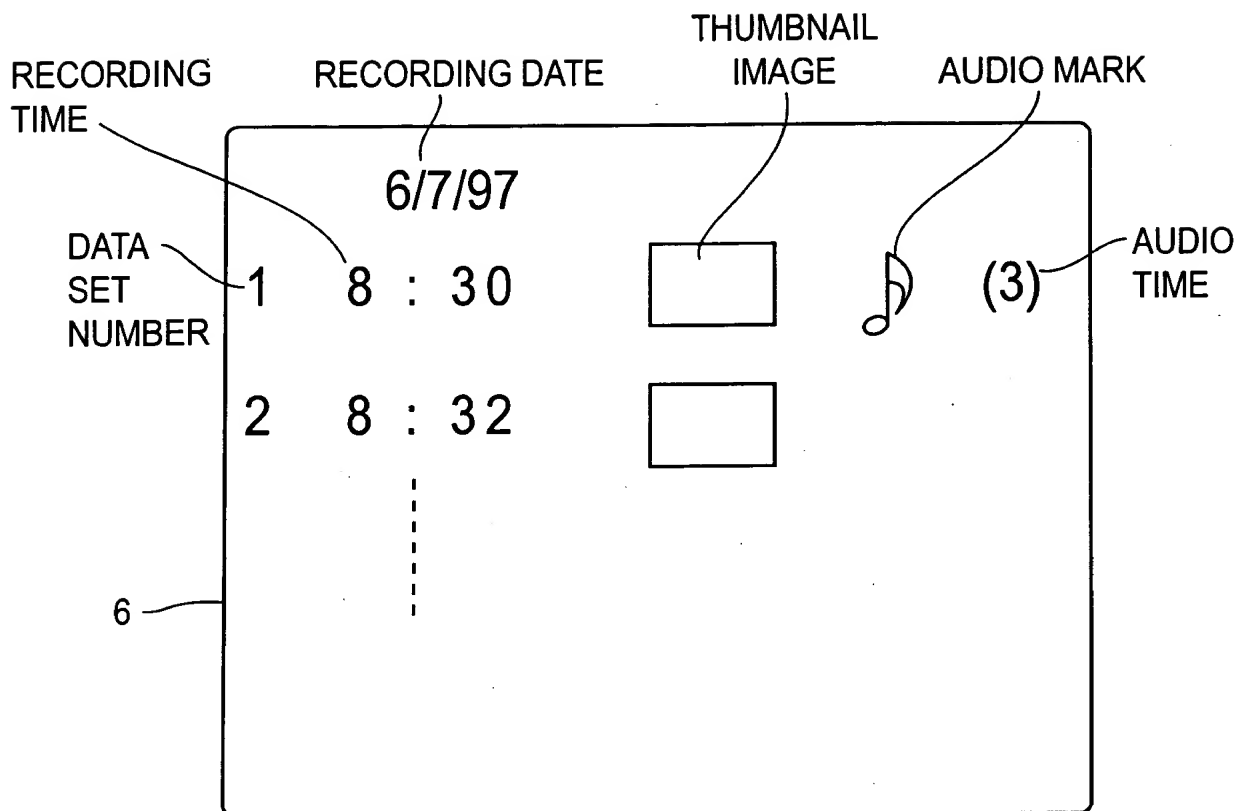


FIG. 13(A)

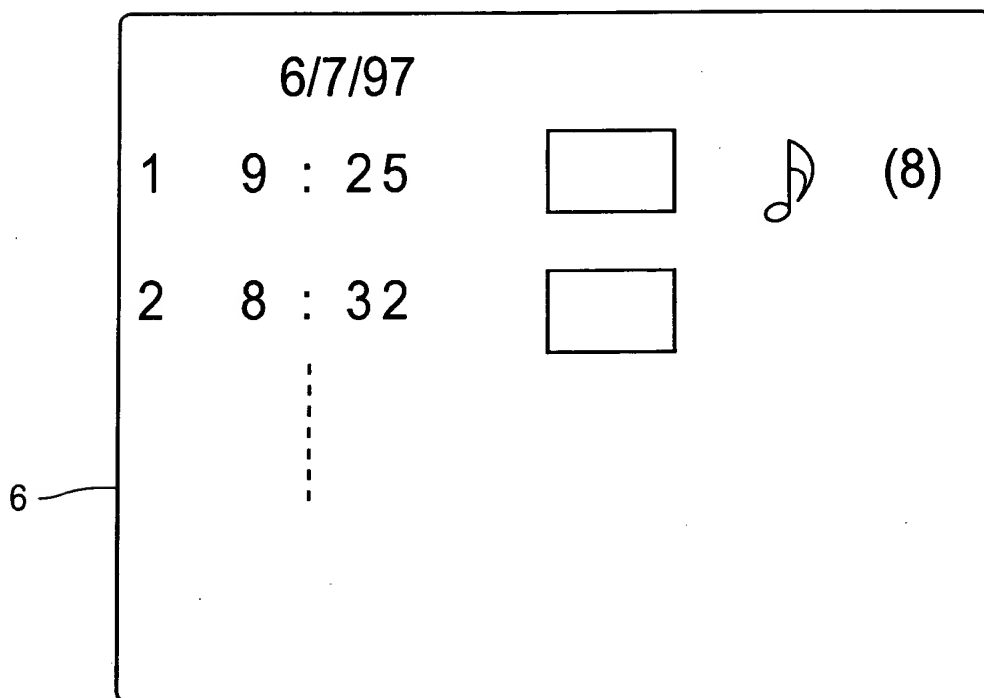


FIG. 13(B)

FIG. 13(C)

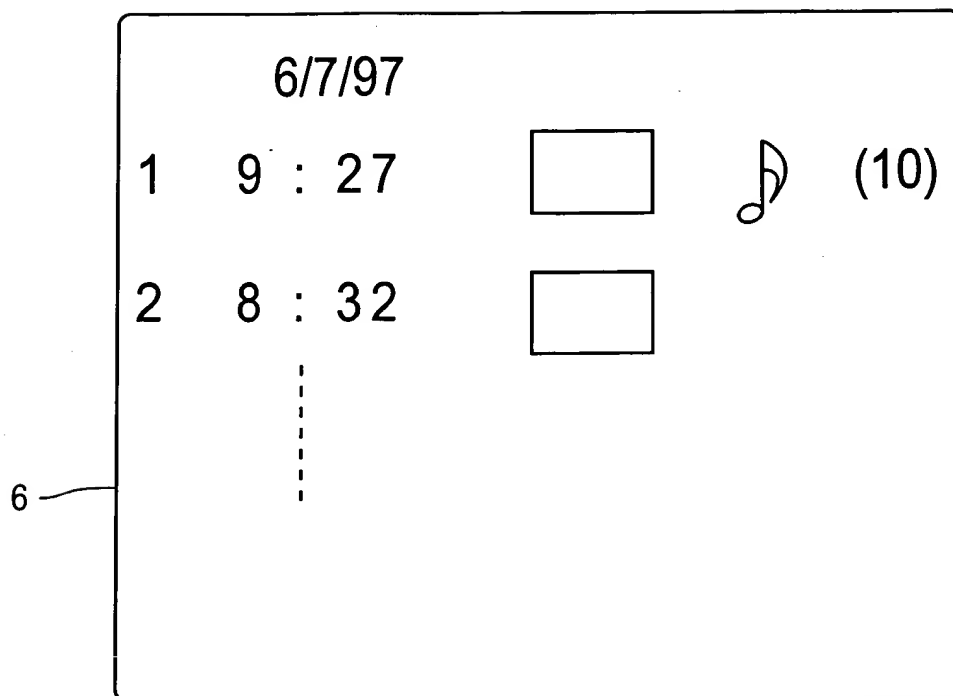


FIG. 13(C)